Reg. No	 Name	22P356

## M. Sc. DEGREE END SEMESTER EXAMINATION : OCTOBER 2022 SEMESTER 3 : PHYSICS

## COURSE: 21P3PHYT11: MICRO ELECTRONICS AND SEMICONDUCTOR DEVICES (EL 1)

(For Regular - 2021 Admission)

	(FOT REGUIAT - 2021 Admission)	
Durat	ion : Three Hours	Max. Weights: 30
	PART A	
	Answer any 8 questions	Weight: 1
1.	Sketch the Ideal Energy band diagram of a metal semiconductor junction.	(A)
2.	Explain what is a system bus?	(E)
3.	Compare the knee voltage of a Schottky diode and that of a semiconductor diode.	(E)
4.	Describe the based index addressing in 8086?	(E)
5.	List out two major differences between microprocessor and microcontroller.	(An)
6.	Describe the function of QS0 and QS1 in 8086.	(An)
7.	What is meant by PUSH and POP in microprocessor?	(A)
8.	List out the classification of instruction set of 8085 microprocessor.	(An)
9.	Which are the interrupts in 8085 microprocessor?	(A)
10.	What is DB? Explain its significance.	(An) (1 x 8 = 8)
	PART B	
	Answer any 6 questions	Weights: 2
11.	Answer any 6 questions  Explain the block diagram of 8051 microcontroller.	Weights: 2
11. 12.	• •	_
	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard	(U)
12.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.	(U) (An)
12. 13.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.  Comment on pointers and index registers of 8086.	(U) (An) (U)
12. 13. 14.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.  Comment on pointers and index registers of 8086.  Explain the general block diagram of 8051 Microcontroller.	(U) (An) (U) (A)
12. 13. 14. 15.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.  Comment on pointers and index registers of 8086.  Explain the general block diagram of 8051 Microcontroller.  Describe the function of Execution unit of 8086 microprocessor.	(U) (An) (U) (A) (E)
12. 13. 14. 15. 16.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.  Comment on pointers and index registers of 8086.  Explain the general block diagram of 8051 Microcontroller.  Describe the function of Execution unit of 8086 microprocessor.  Explain flag registers of 8086 with block diagram	(U) (An) (U) (A) (E) (A) (A)
12. 13. 14. 15. 16. 17.	Explain the block diagram of 8051 microcontroller.  Explain the differences between Von Neumann architecture vs Harvard architecture.  Comment on pointers and index registers of 8086.  Explain the general block diagram of 8051 Microcontroller.  Describe the function of Execution unit of 8086 microprocessor.  Explain flag registers of 8086 with block diagram  Describe the characteristics of static RAM.  Describe in detail the general purpose data registers and status registers of 8086 microprocessor.	(U) (An) (U) (A) (E) (A) (A)
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21. Explain the PORT circuitry available in 8051.

(An) (5 x 2 = 10)

List the maximum and minimum mode signals of 8086. 22.

(An)

**OBE: Questions to Course Outcome Mapping** 

СО	Course Outcome Description	CL	Questions	Total Wt.
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Cognitive Level (CL): Cr - CREATE; E - EVALUATE; An - ANALYZE; A - APPLY; U - UNDERSTAND; R - REMEMBER;